

10/13/02

**U.S. UTILITY Patent Application**

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10044910	01/15/2002	361	793	2807 28564	111

**\*\*APPLICANTS:** Okabe Hiroshi; Yamada Hirozi;

**\*\*CONTINUING DATA VERIFIED:**

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-047546 02/23/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	XA-9611
Verified and Acknowledged Examiners's initials			

**TITLE :** Electronic circuit equipment using multilayer circuit board

U.S. DEPT. OF COMM./PAT. & TM-PTO-4361 (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			<b>Total Claims</b>	<b>Print Claim for O.G</b>
<b>ISSUE FEE</b>			<b>DRAWING</b>	
<b>Amount Due</b>	<b>Date Paid</b>		<b>Sheets Drwg.</b>	<b>Figs.Drwg.</b>
		<b>Primary Examiner</b>		
		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		

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